

CURRENT SENSE TRANSFORMERS

1. Maximum ratings specified with rated secondary terminating resistance and 1 turn primary..
2. Amp-microsecond (AuSEC.) rating of primary equals volt-microsecond (VuSEC.) rating of secondary when secondary is terminated in rated resistance. (Amp-microseconds is equal to the product of a square pulse of current, in amps, times the current pulse width in microseconds.)
3. Maximum Operating Temperature.
4. When terminated with rated terminating resistance the inductor scale factor is $V_{out}=1$ Volt per amp.for center tapped units terminating resistance for each half of winding is listed value divided by 2.
5. Primary furnished.
6. Designed for switching power supply applications.
7. Reinforced insulation per IEC380.
8. 3,750V RMS primary to secondary HI-POT.
9. Frequency range 20KHz plus.
10. Encapsulated construction.

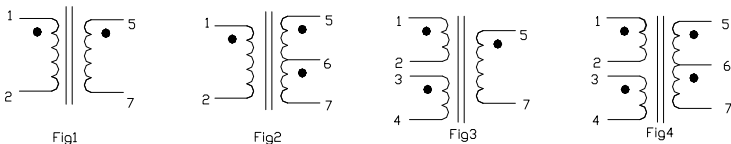


Electrical Characteristics:

PART NUMBER*	SCHEMATIC FIGURE	PART NUMBER*	SCHEMATIC FIGURE	SECONDARY TURNS	SECONDARY INDUCTANCE (Mh) Nom	SECONDARY DCR (OHMS) Max	SECONDARY RATED TERMINATING (OHMS) Nom
5T6230-01X	1	5T6230-17X	3	50	5	1	50
5T6230-02X	1	5T6230-18X	3	100	20	2	100
5T6230-03X	1	5T6230-19X	3	200	80	8	200
5T6230-04X	1	5T6230-20X	3	300	180	16	300
5T6230-05X	1	5T6230-21X	3	50	5	0.7	50
5T6230-06X	1	5T6230-22X	3	100	20	1.4	100
5T6230-07X	1	5T6230-23X	3	200	80	4.5	200
5T6230-08X	1	5T6230-24X	3	300	180	11.0	300
5T6230-09X	2	5T6230-25X	4	50CT	5	1	50
5T6230-10X	2	5T6230-26X	4	100CT	20	2	100
5T6230-11X	2	5T6230-27X	4	200CT	80	8	200
5T6230-12X	2	5T6230-28X	4	300CT	180	16	300
5T6230-13X	2	5T6230-29X	4	50CT	5	0.7	50
5T6230-14	2	5T6230-30X	4	100CT	20	1.4	100
5T6230-15X	2	5T6230-31X	4	200CT	80	4.5	200
5T6230-16X	2	5T6230-32X	4	300CT	180	11.0	300

*Configuration: X noting use type--S=Surface-mount and P=Plug-in

Mechanical Dimensions:

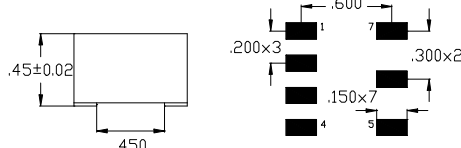
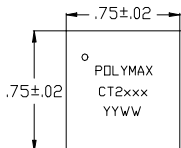


Schematics

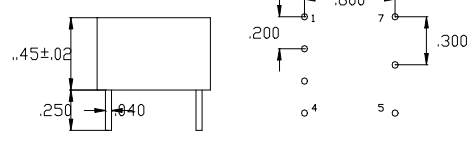
Surface mount construction

Plug-in construction

Dimensions: Inches
Unless otherwise specified,
all tolerances are .010
White dot indicates pin #1



Suggested pcb layout



Suggested pcb layout